

JNTUH COLLEGE OF ENGINEERING: HYDERABAD
(AUTONOMOUS)

B.Tech. III year II Semester, Supplementary Examinations, APRIL,
2015

Upto '08' Batch

EXAMINATION TIME – TABLE

Exam Time: 10.00AM to 01.00 PM

Date / Day	ECE	CSE
27/04/2015	Digital Signal Processing	Computer Graphics
29/04/2015	VLSI Design	-
1/5/2015	Microwave Engineering	-
4/5/2015	Microprocessor & Interfacing	-

Date:21-04-2015

Sd/-
PRINCIPAL